

Fig. 2a PRIOR ART

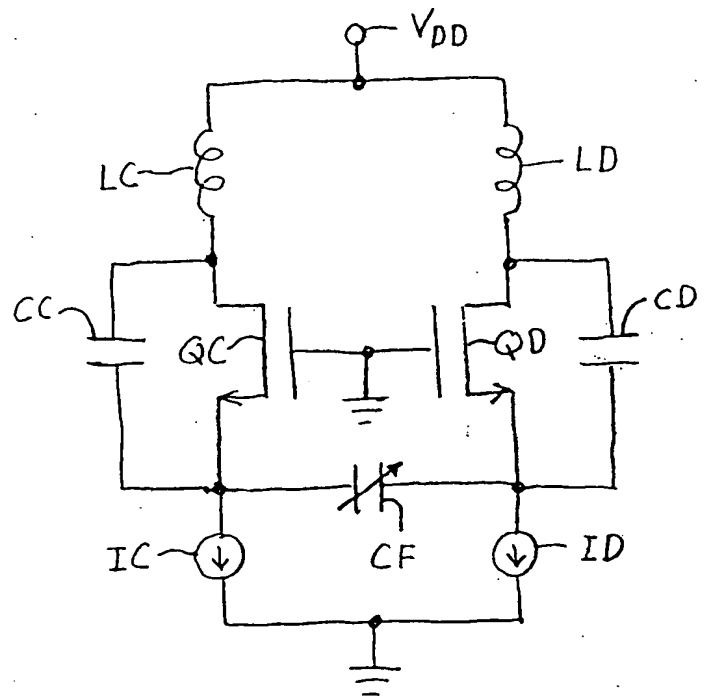


Fig. 2b PRIOR ART

Fig. 3

PRIOR ART

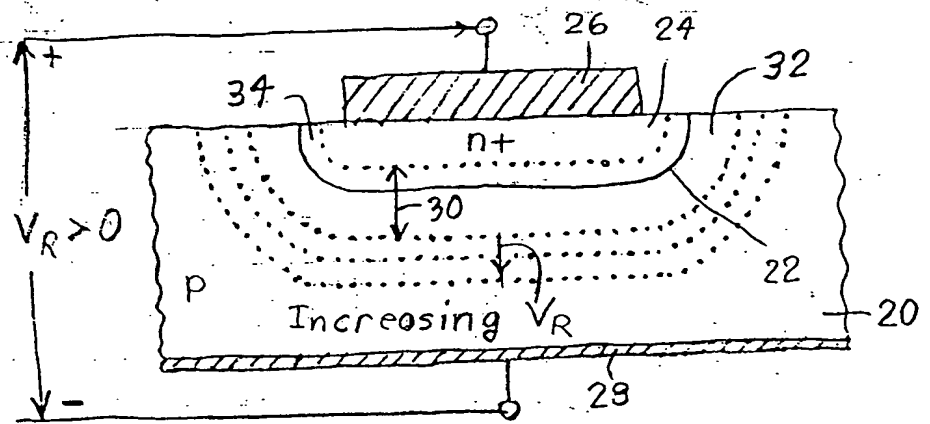


Fig. 4

PRIOR ART

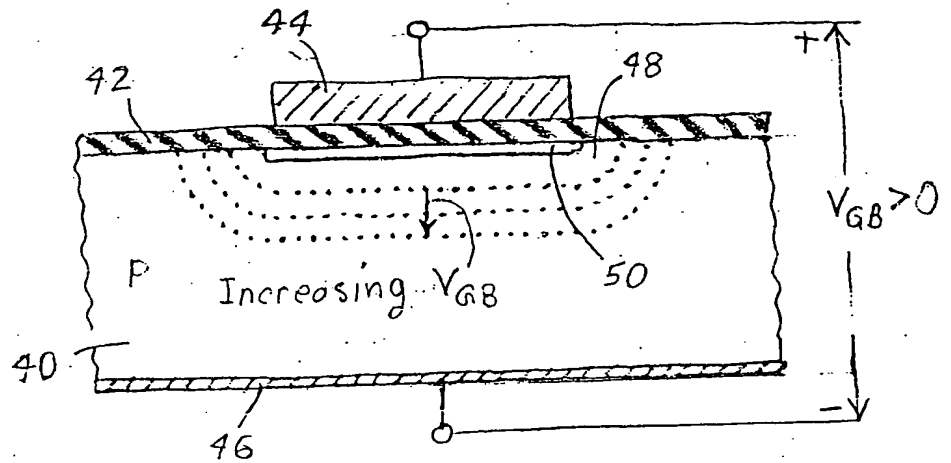
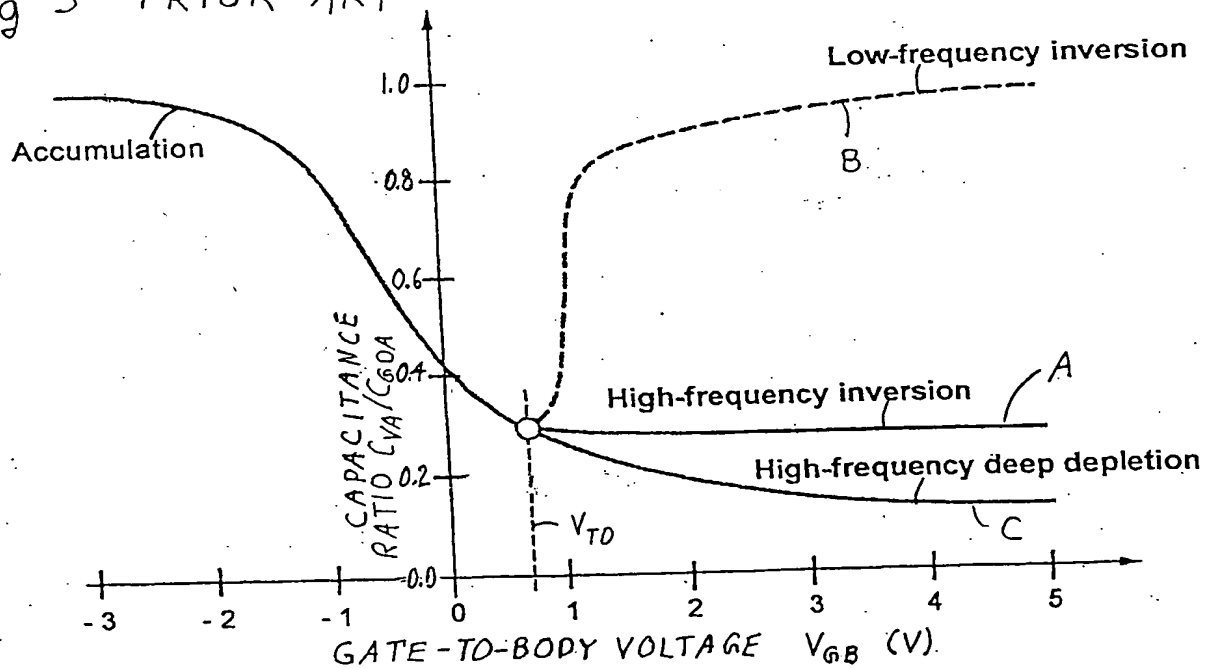


Fig 5 PRIOR ART



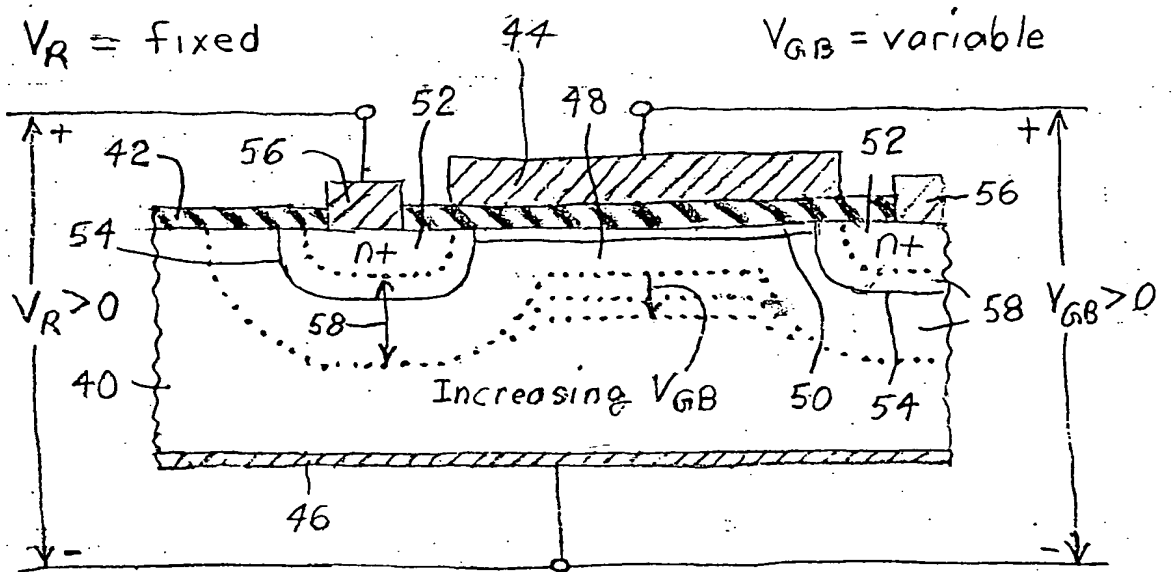


Fig. 6 PRIOR ART

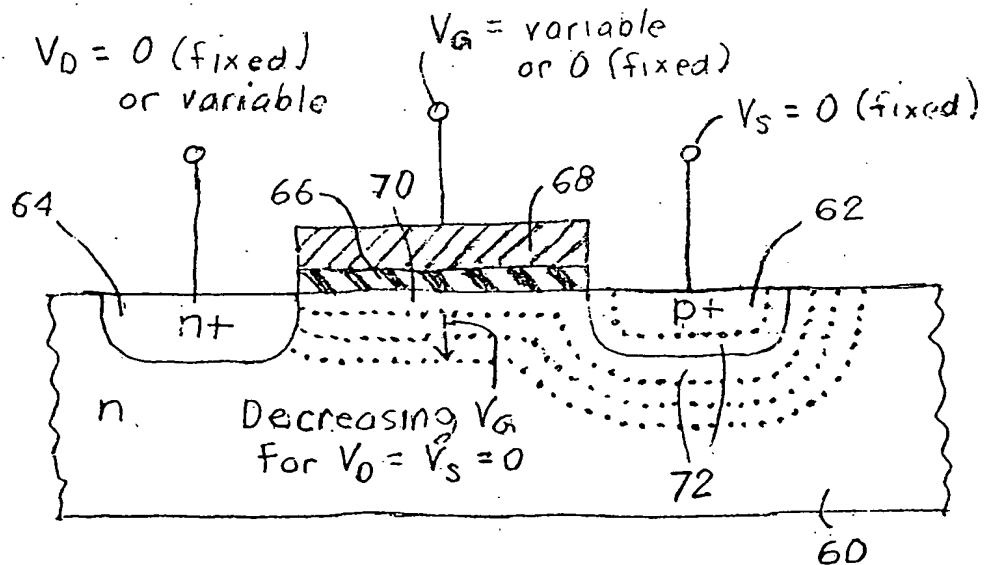
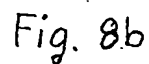
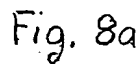


Fig. 7 PRIOR ART



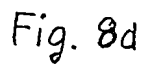
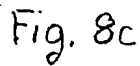
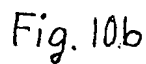
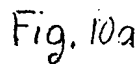
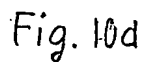
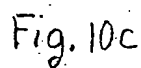


Fig. 9a

Fig. 9b





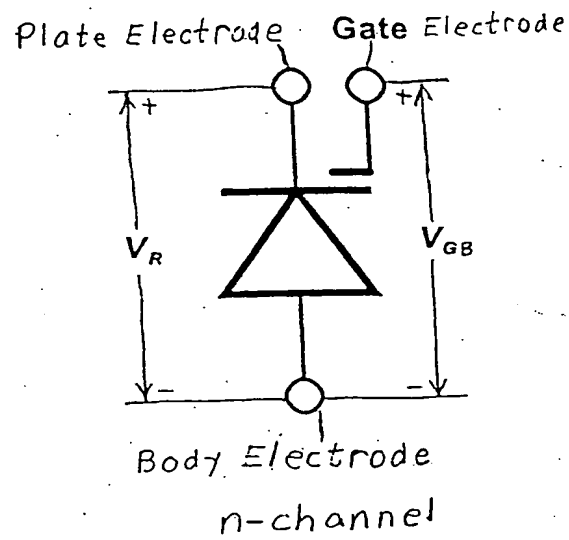


Fig. 11a

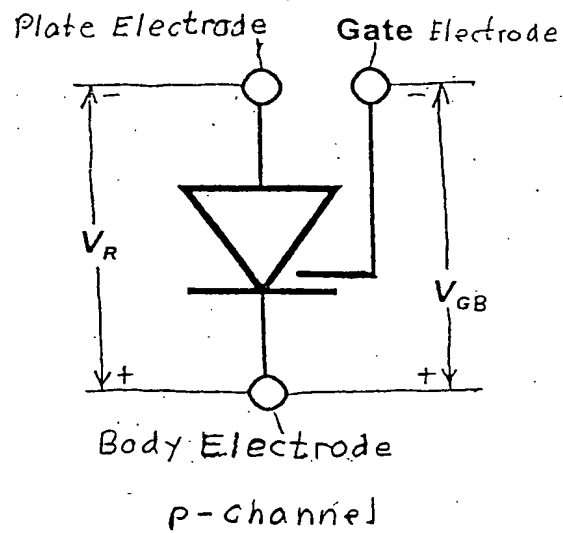


Fig. 11b

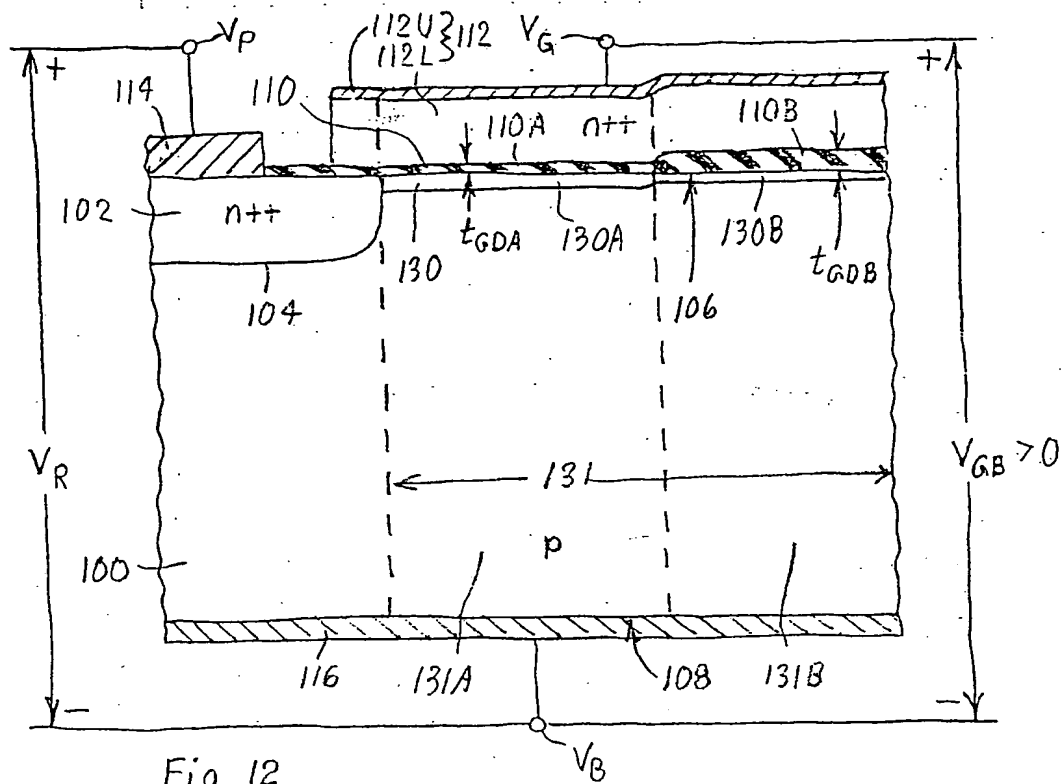


Fig. 12

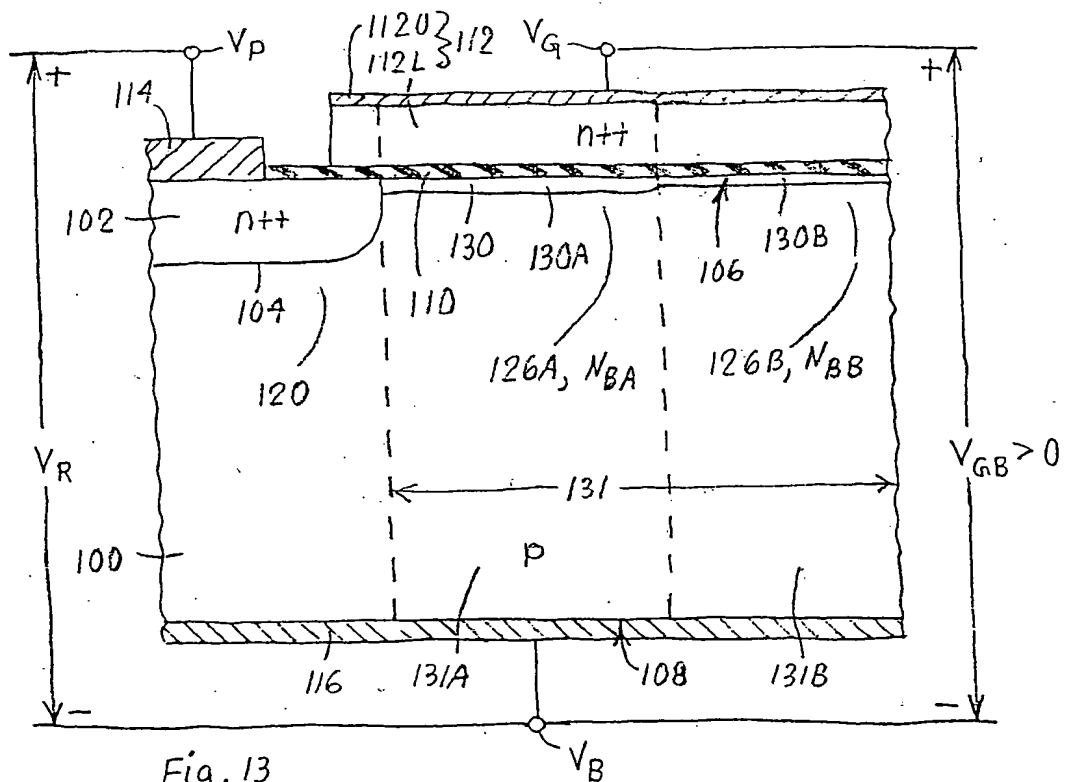
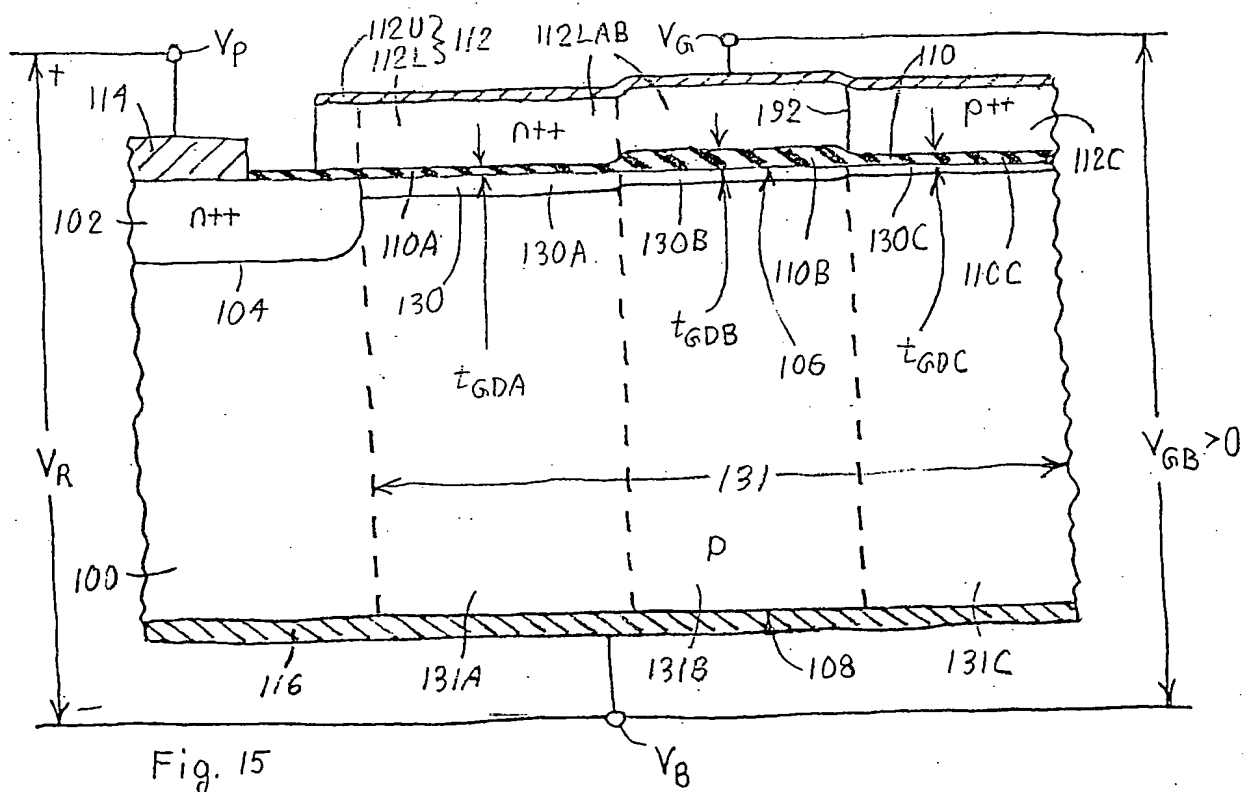
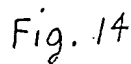


Fig. 13



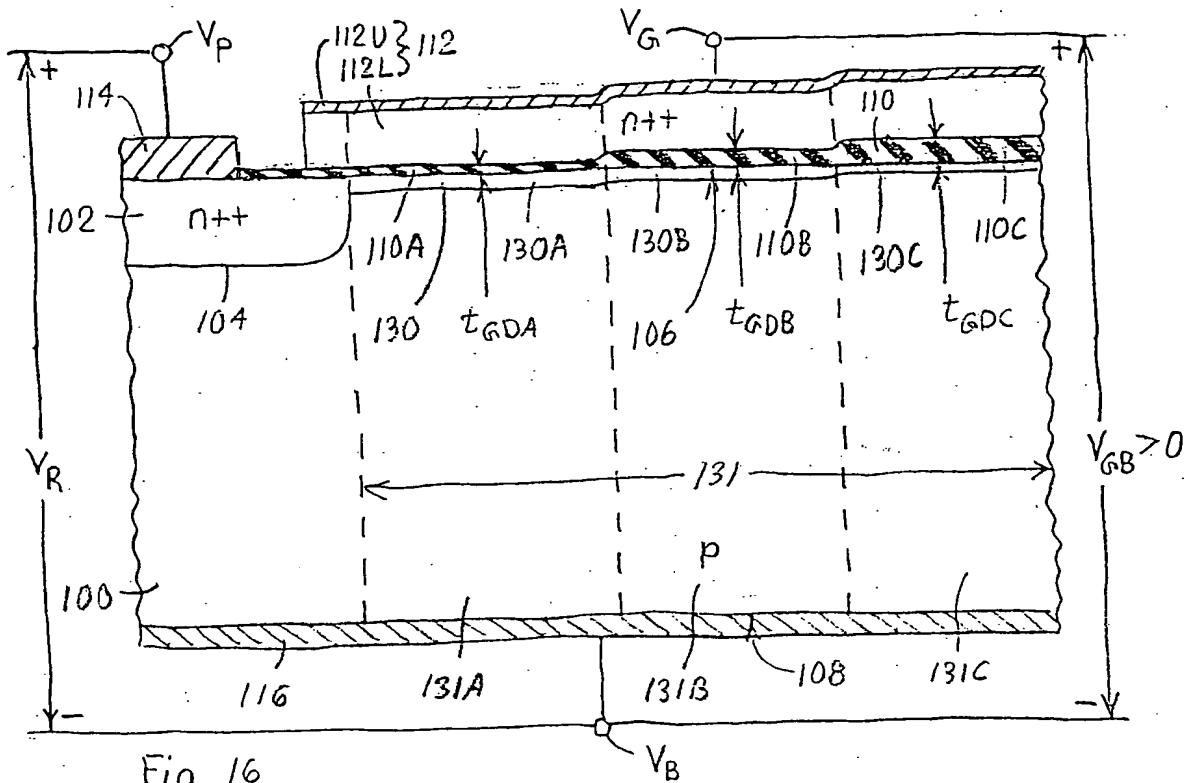


Fig. 16

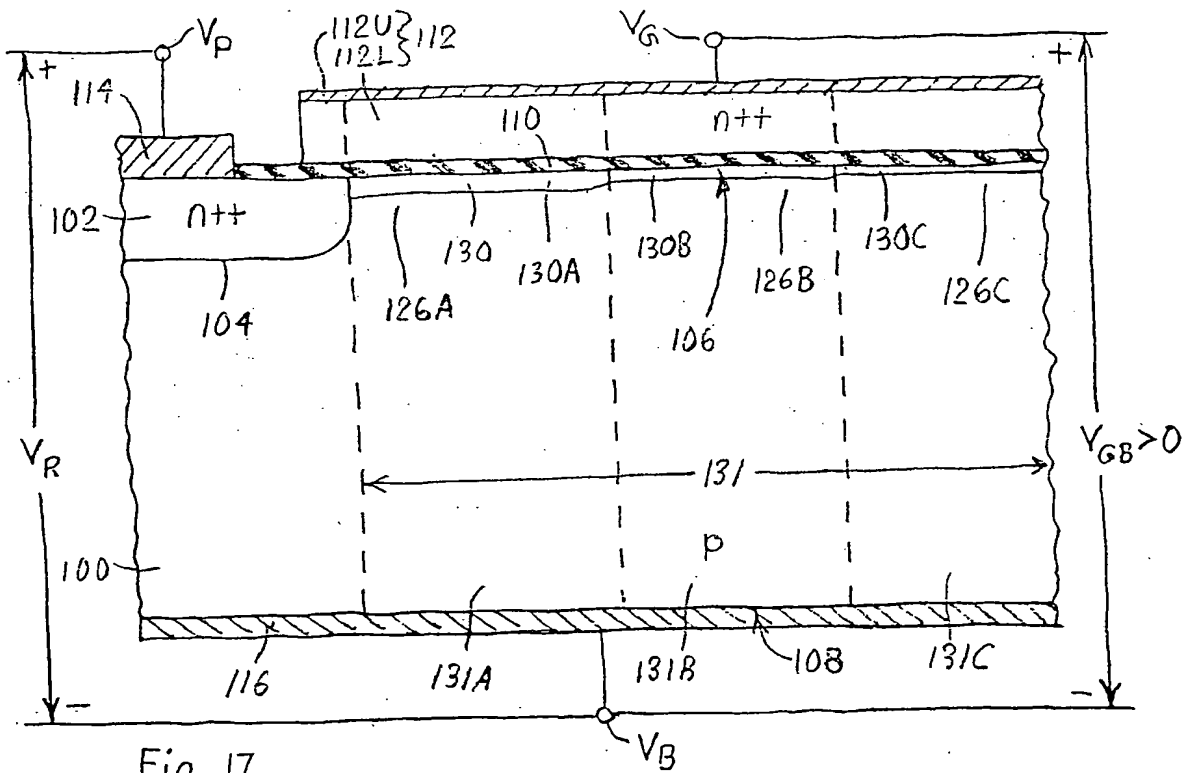


Fig. 17

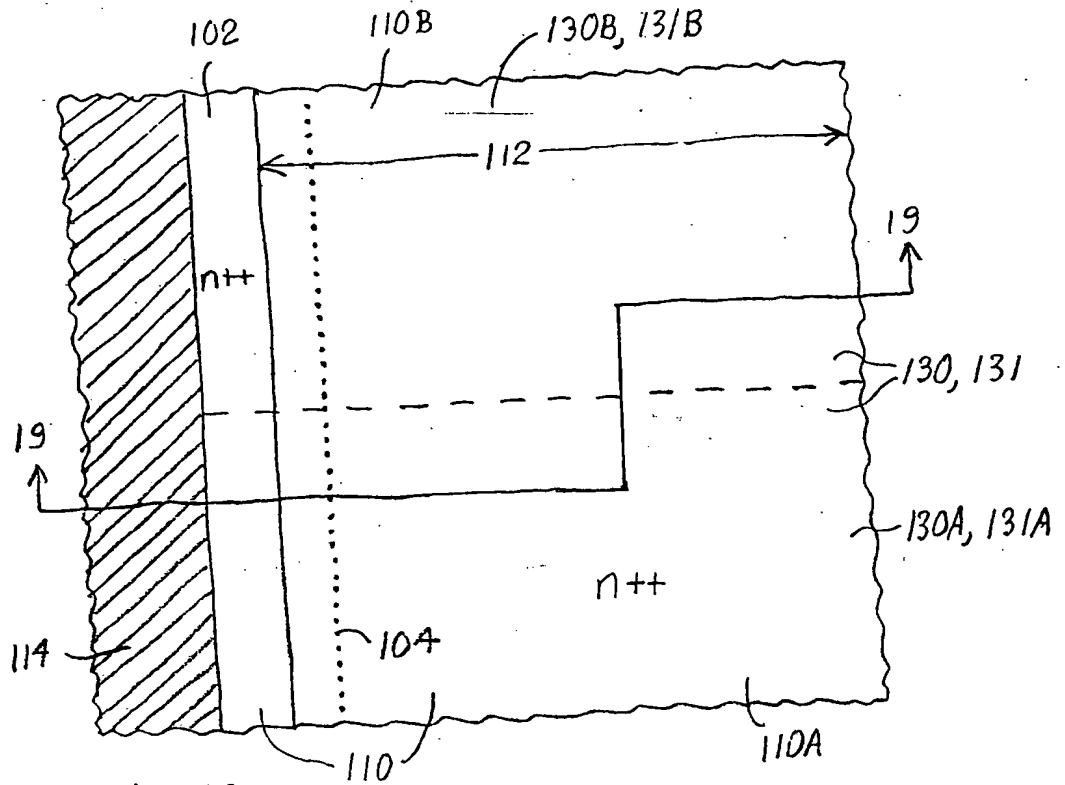


Fig. 18

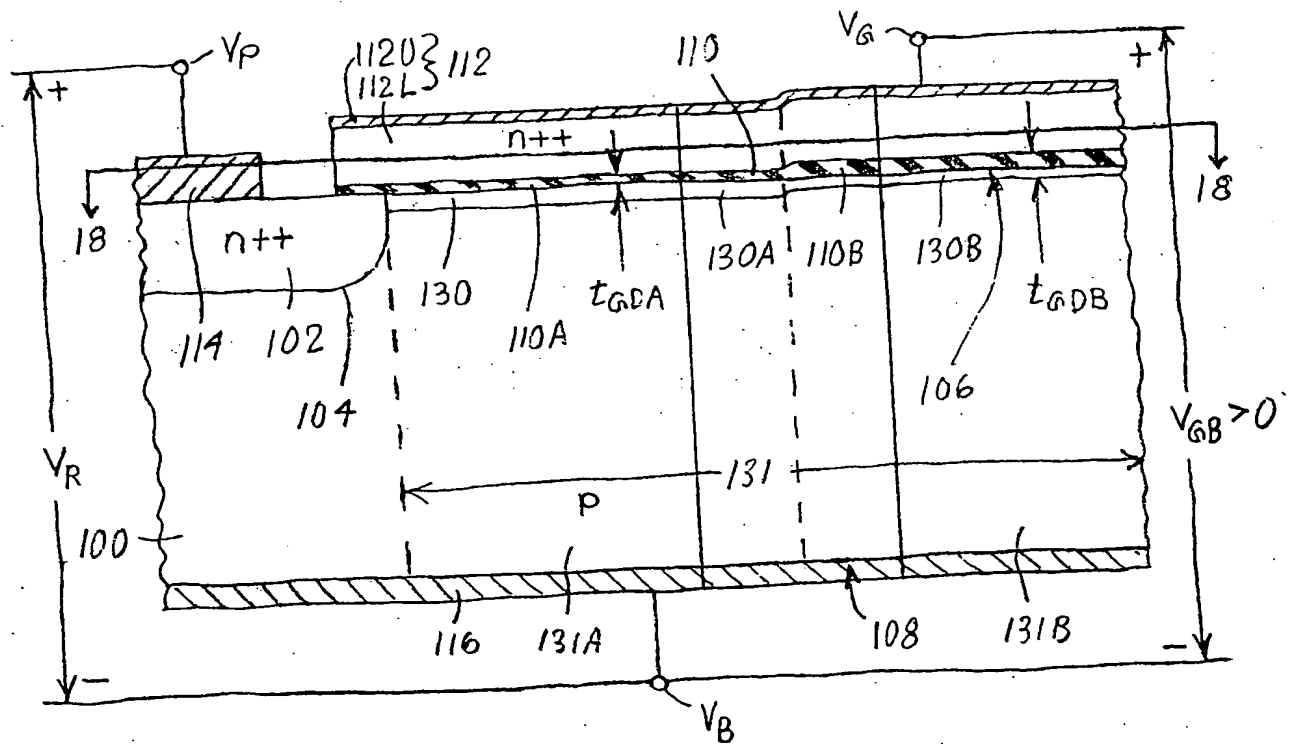
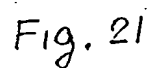
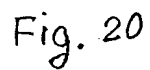
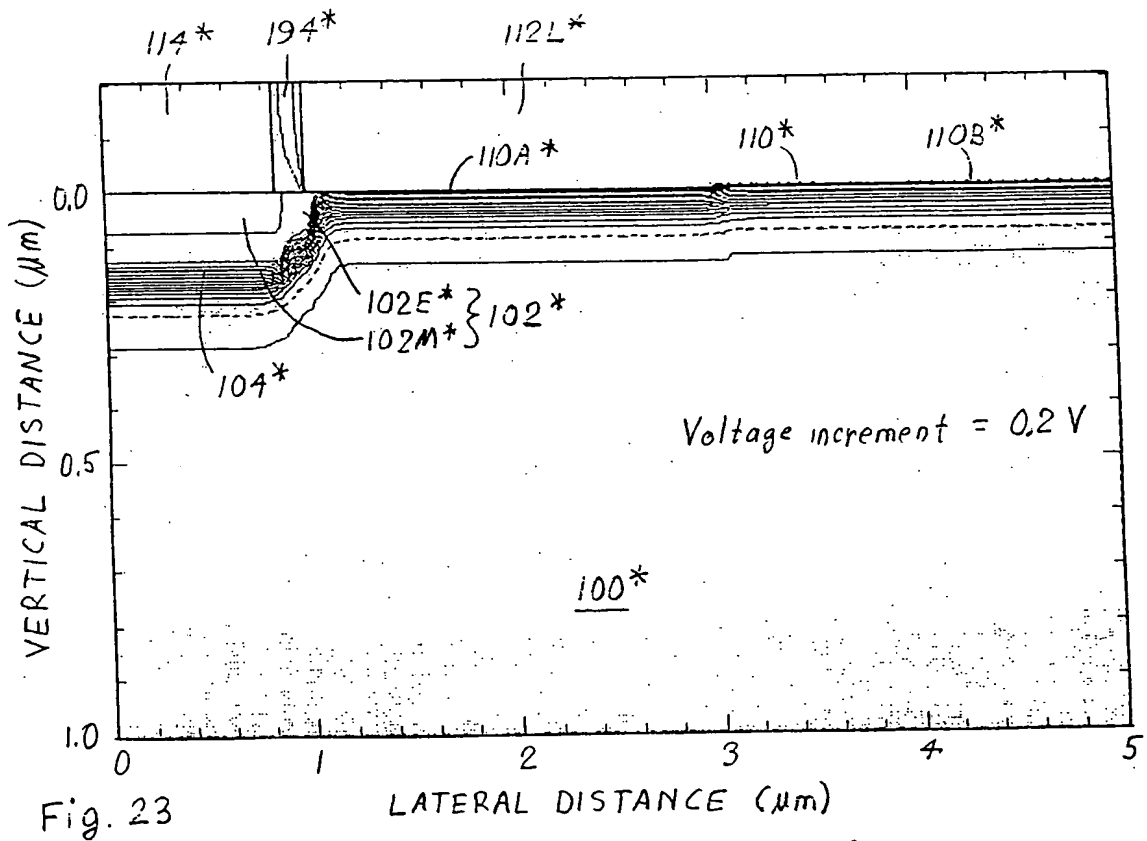
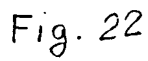
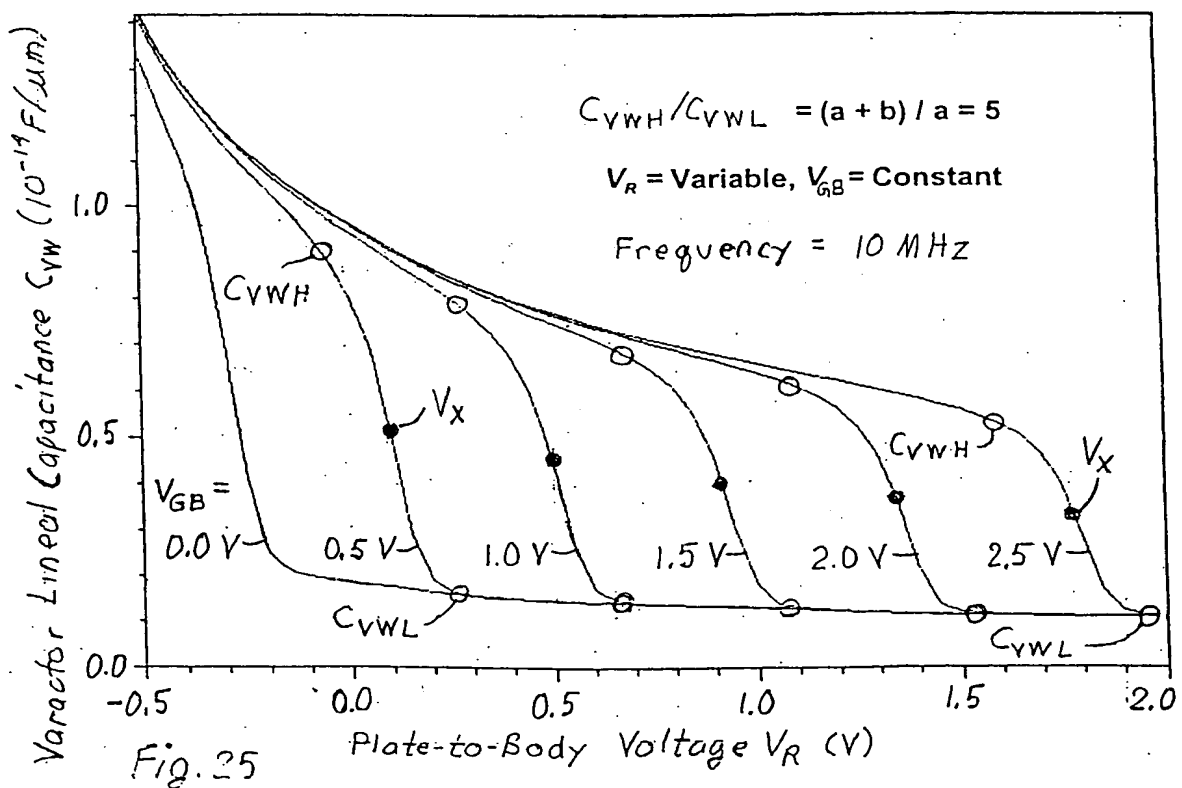
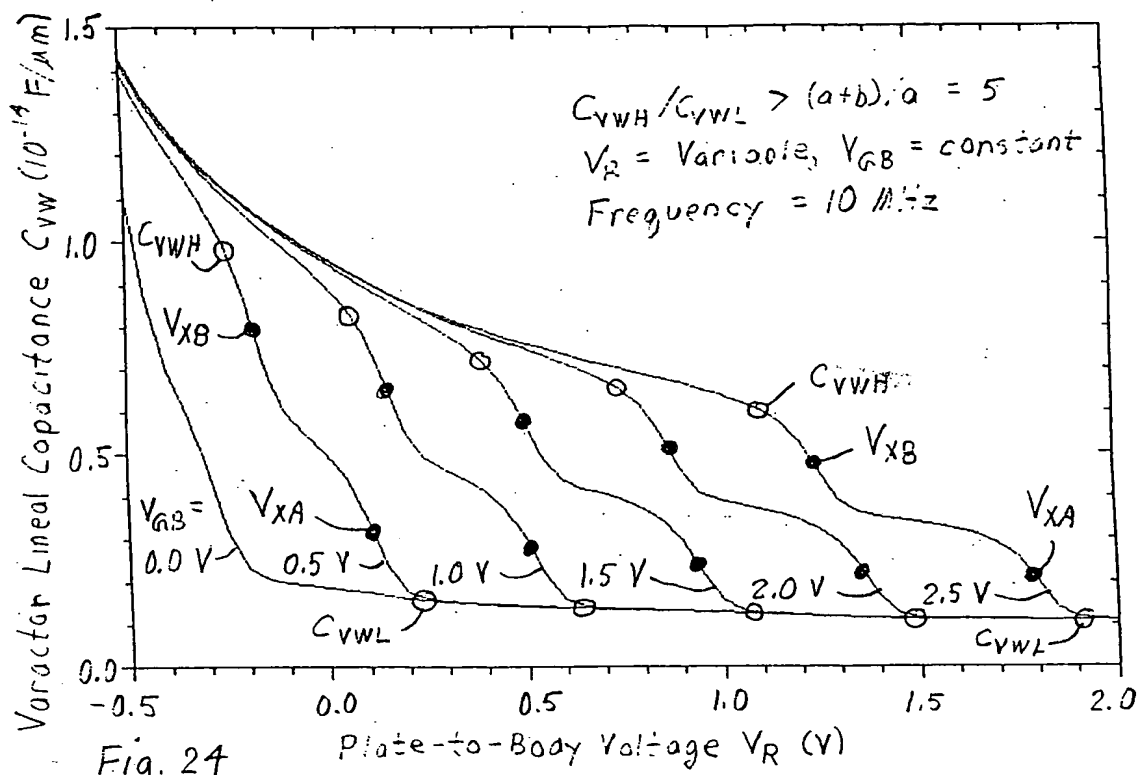


Fig. 19





NS-5127



NS-5127

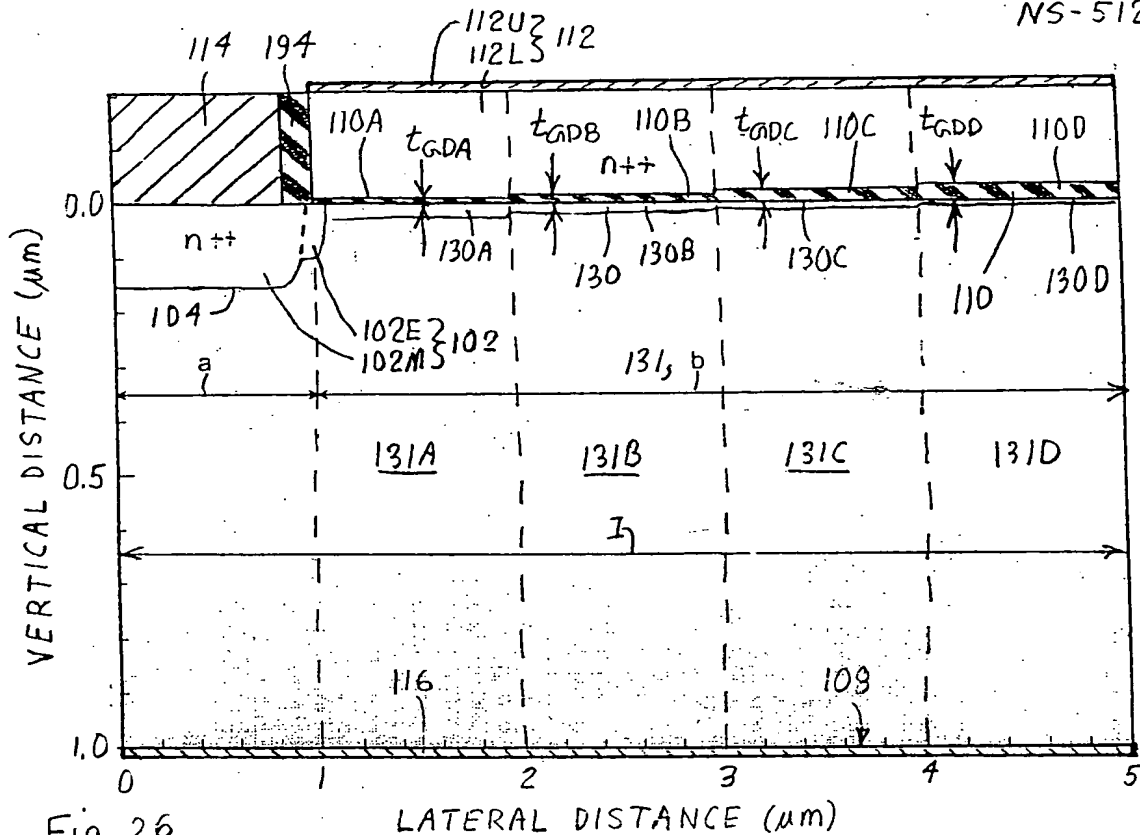


Fig. 26

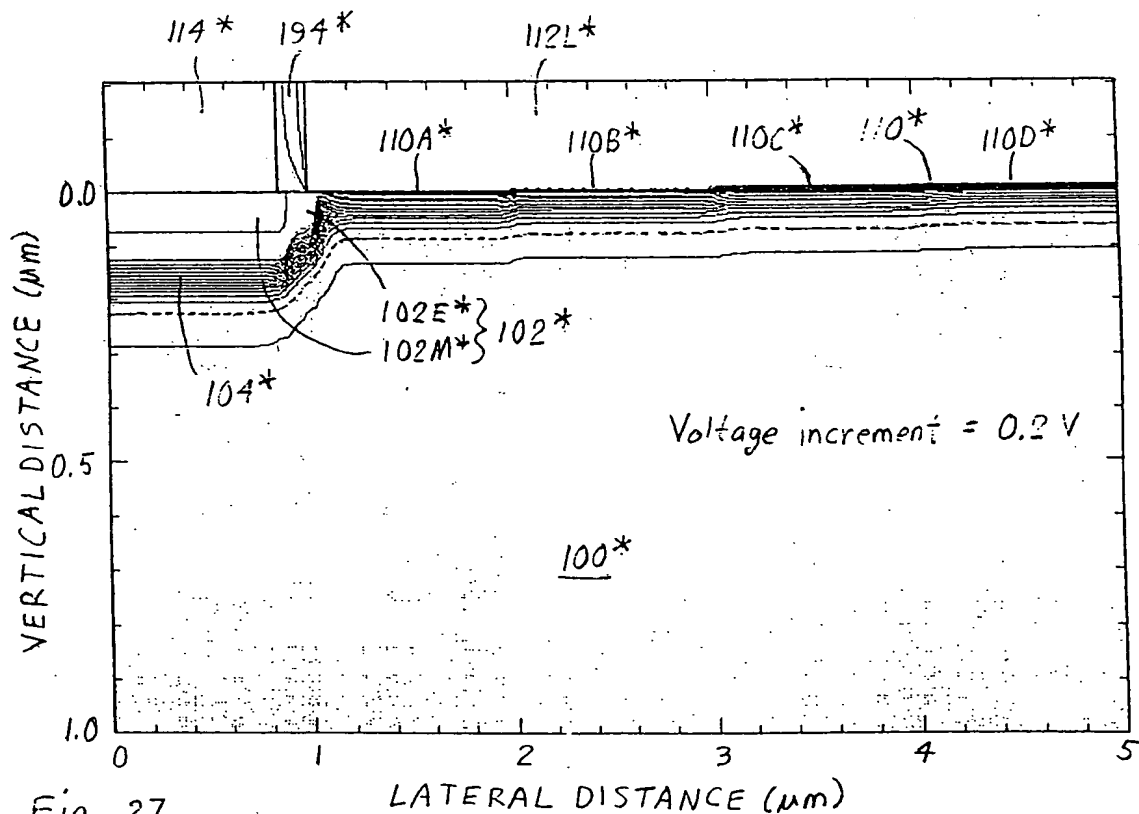


Fig. 27

NS-5127

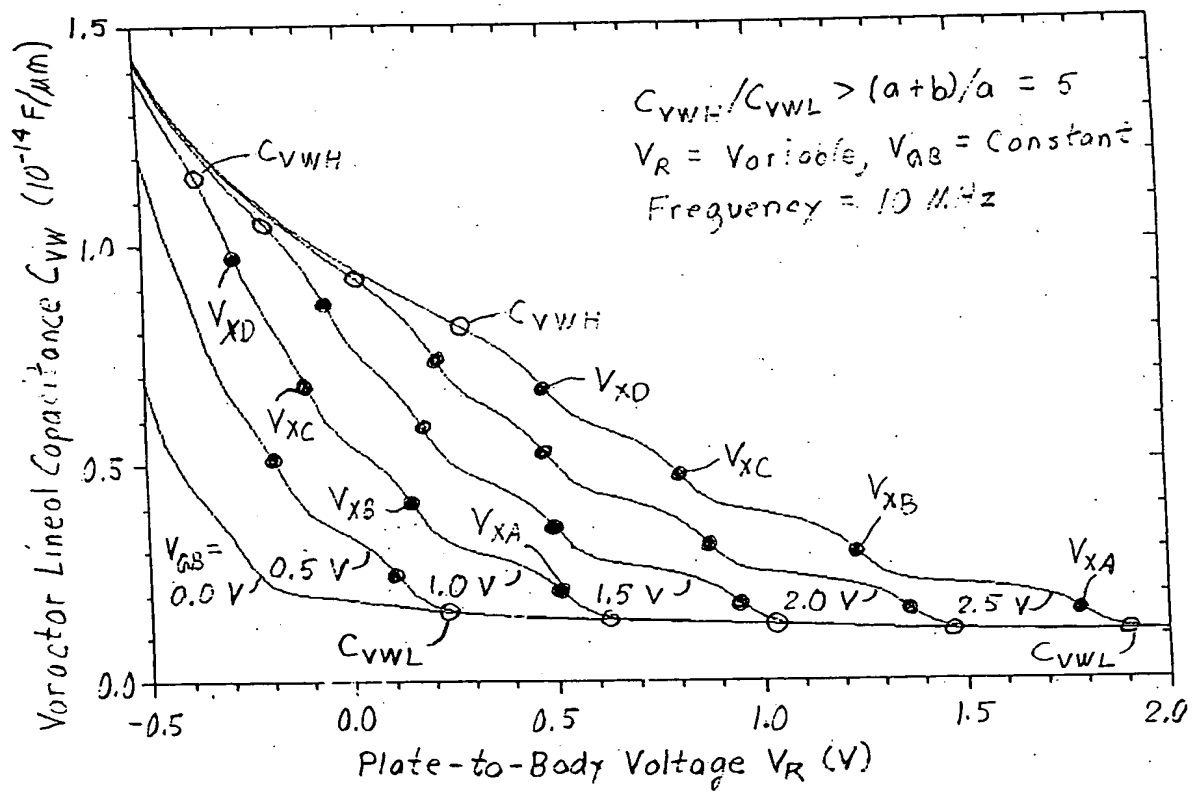
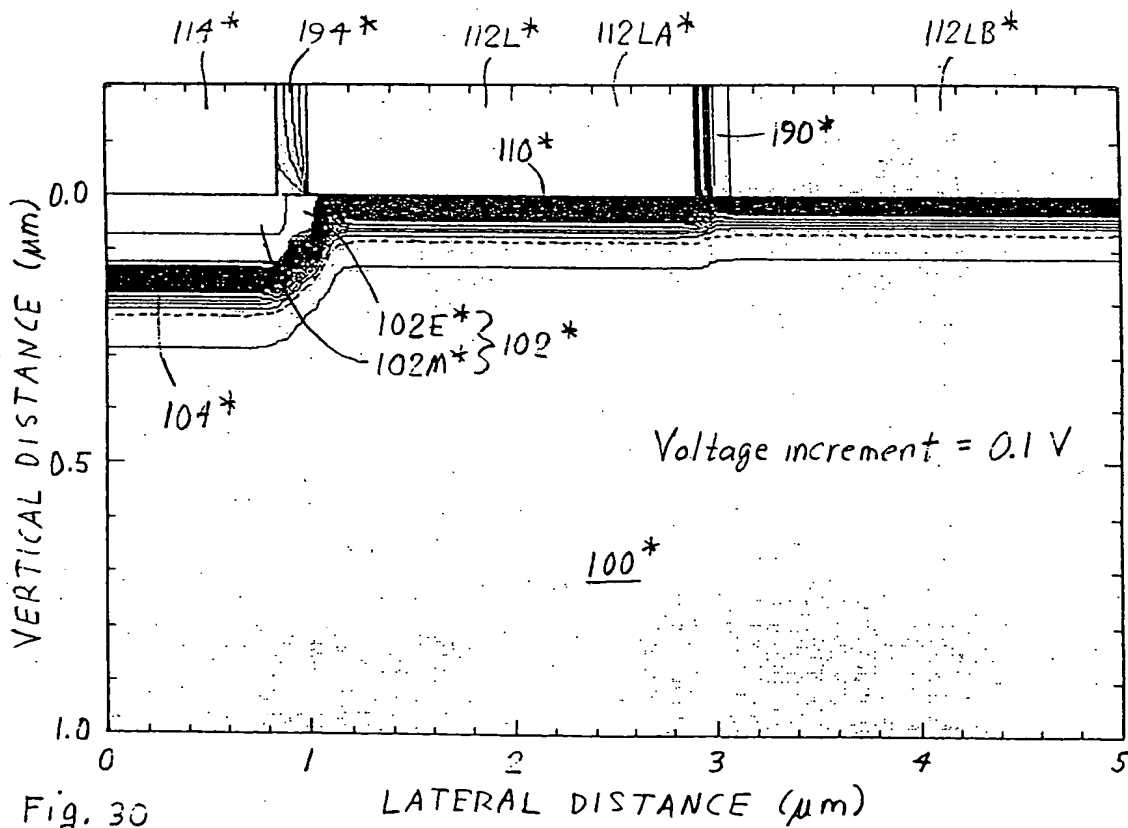
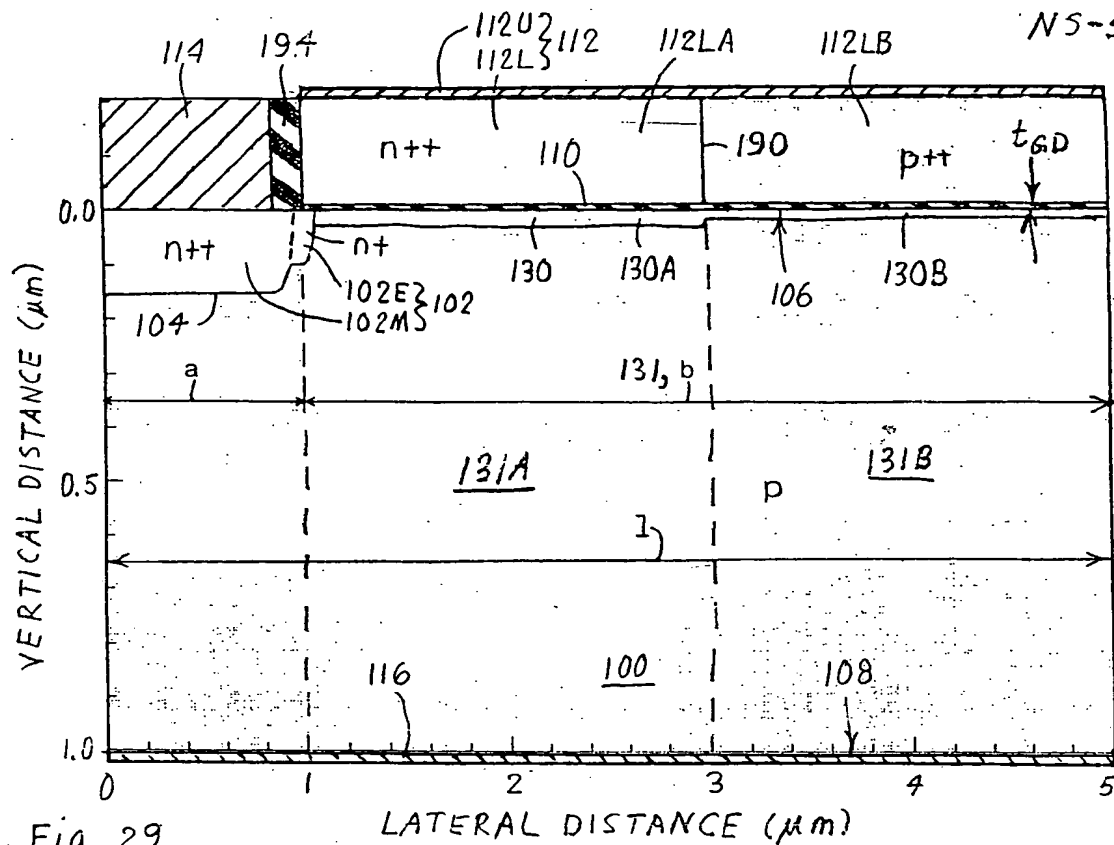


Fig. 28.

NS-5127



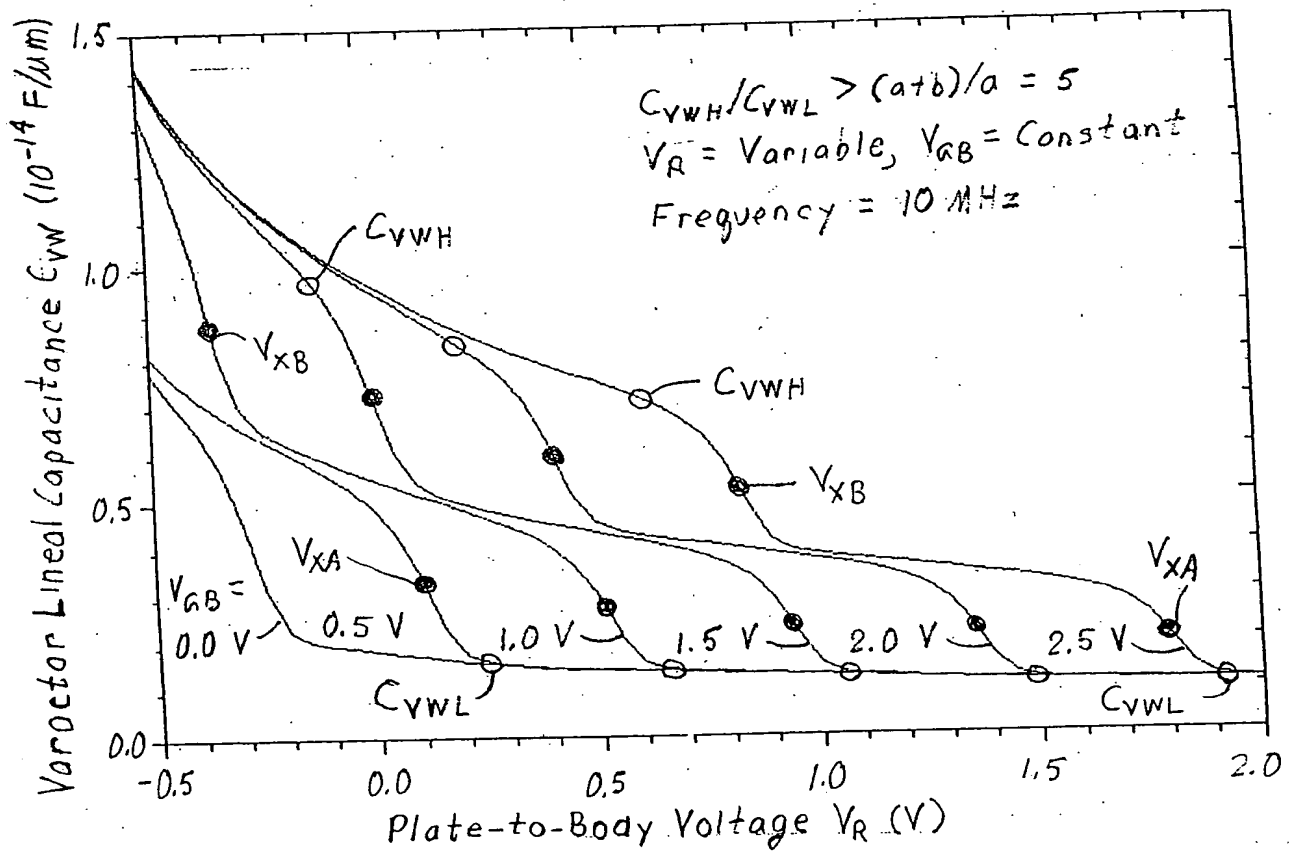


Fig. 31

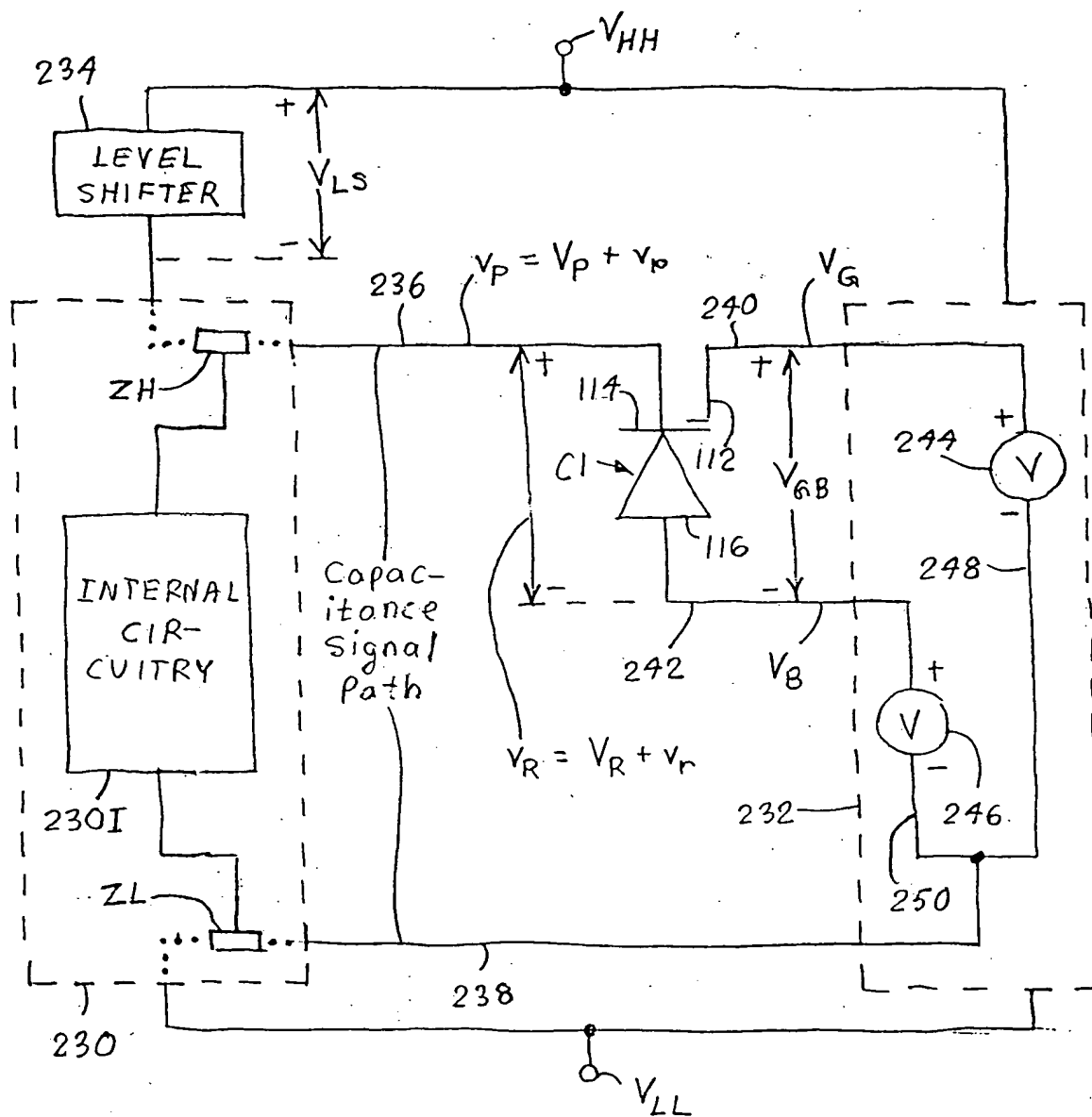
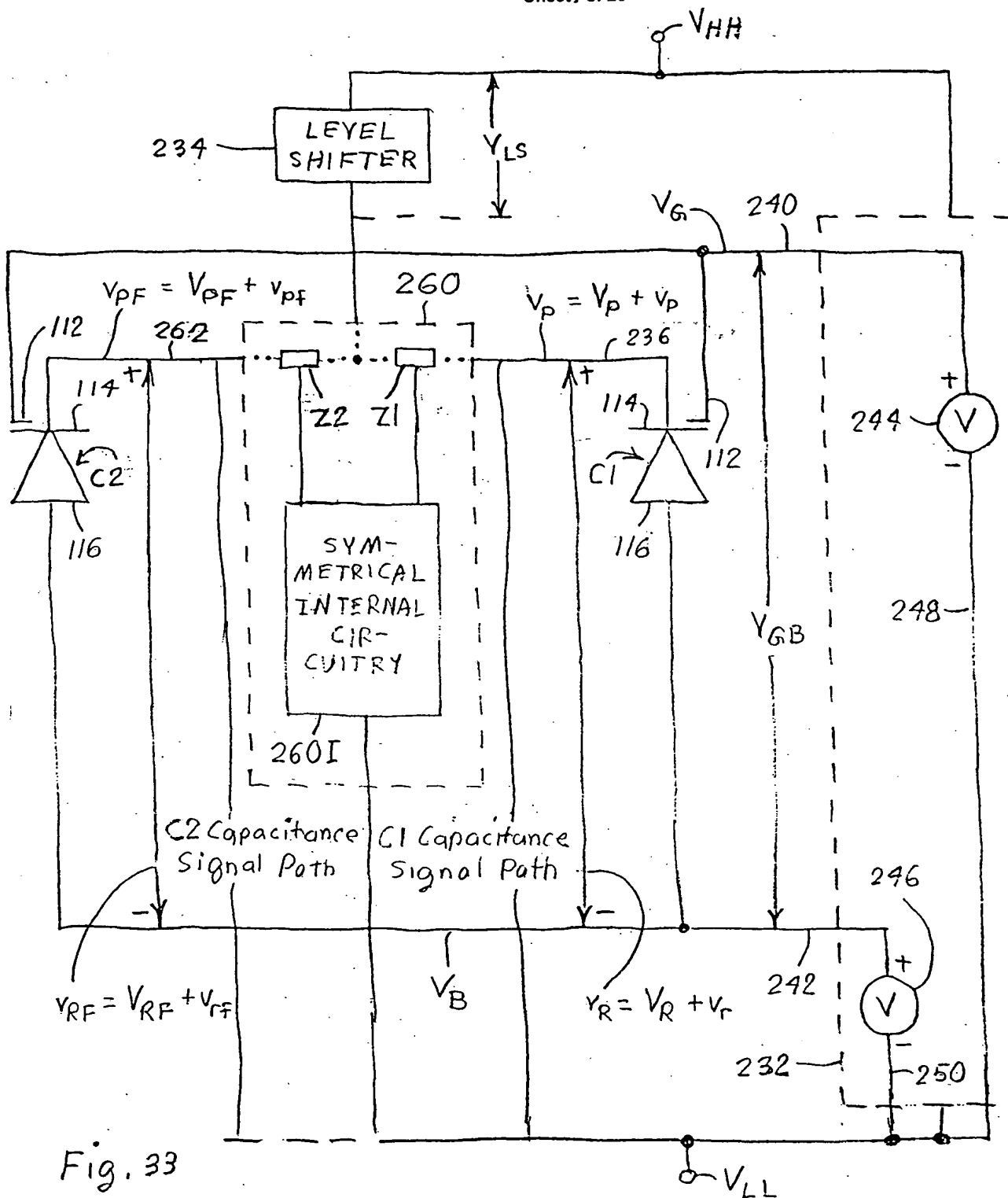


Fig. 32

Gate-Enhanced Junction Varactor With Gradual Capacitance Variation

Constantin Buluccea NS-5127 US
Sheet 7 of 26



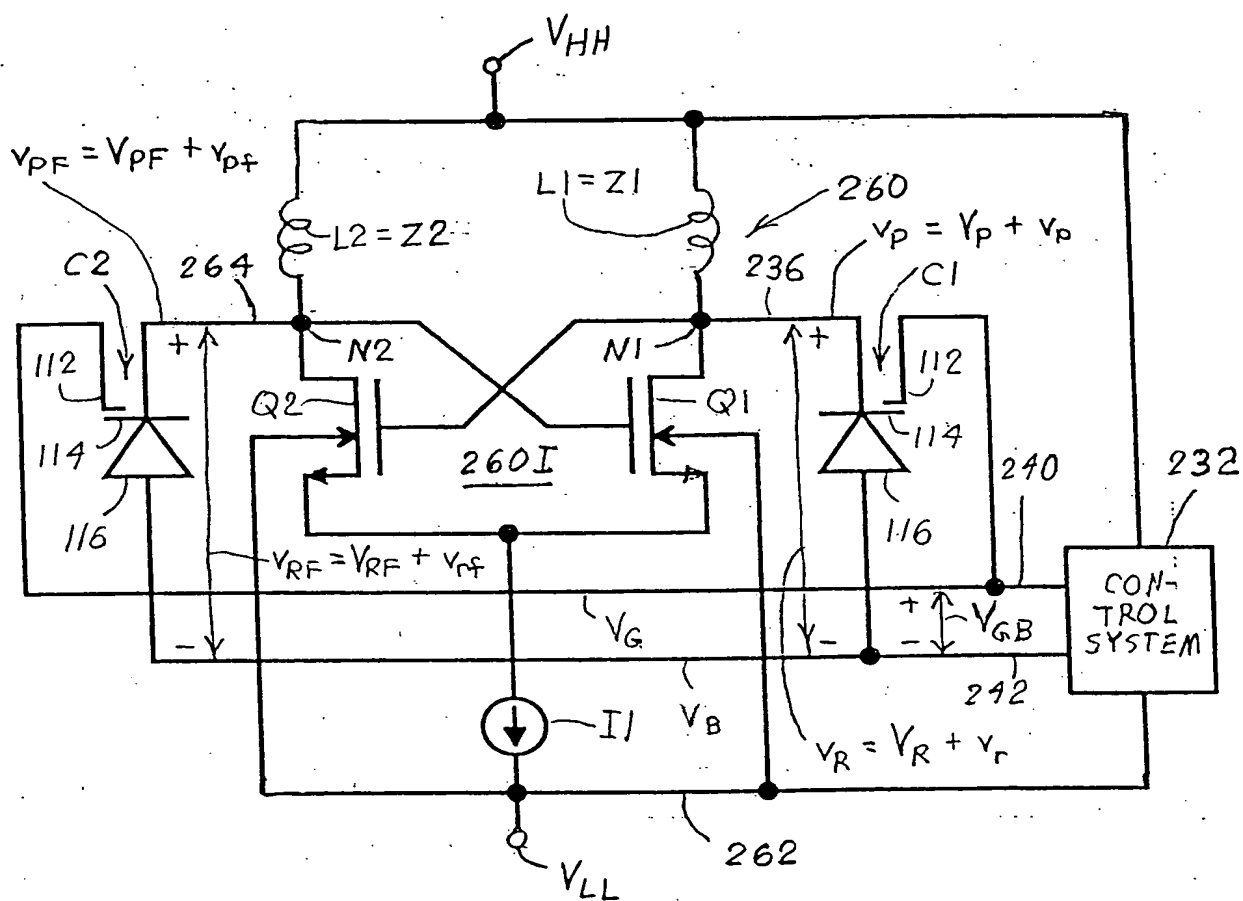


Fig. 34a

Gate-Enhanced Junction Varactor With Gradual
Capacitance Variation
Constantin Bulucea NS-5127 US
Sheet 7 of 26

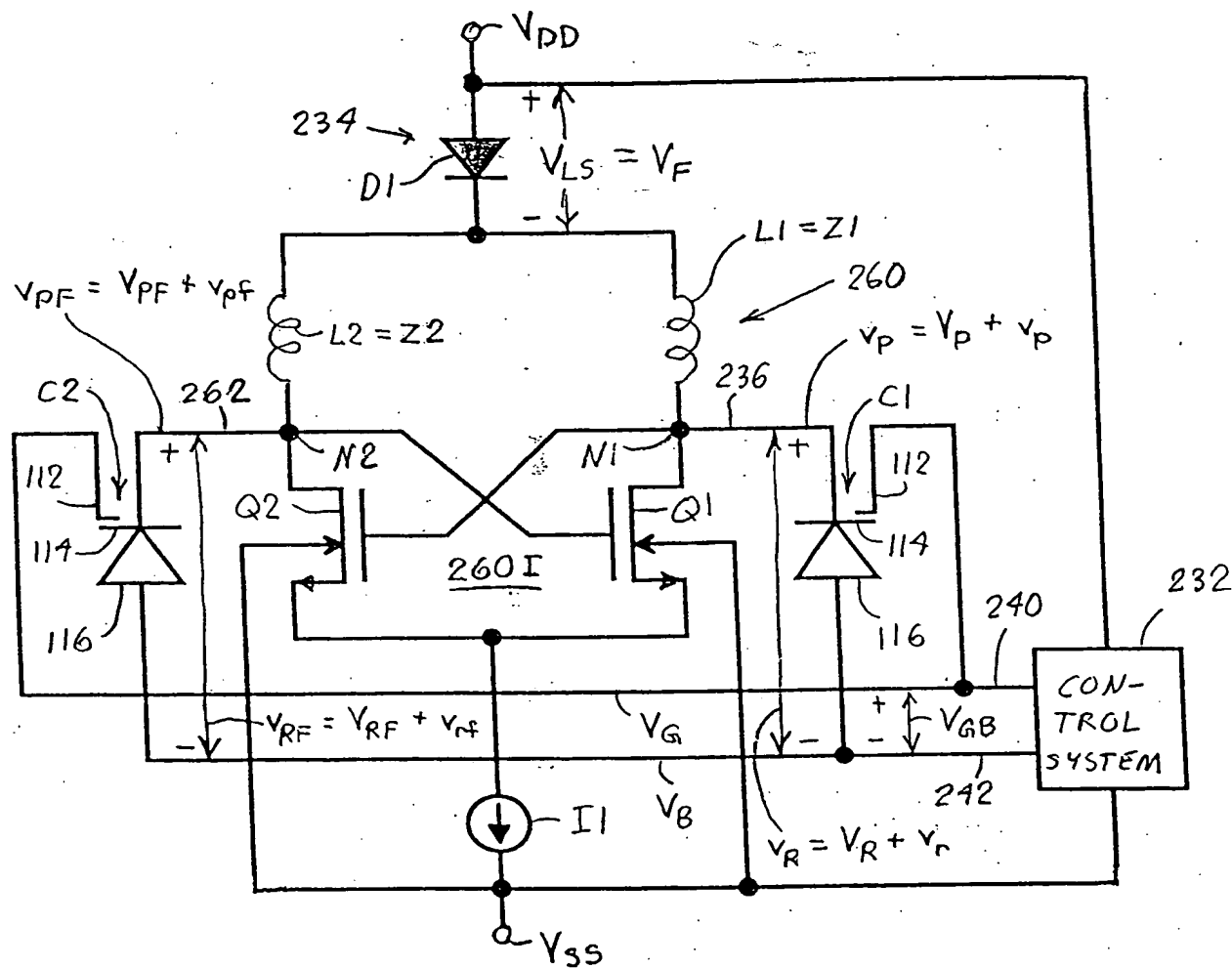


Fig. 34b

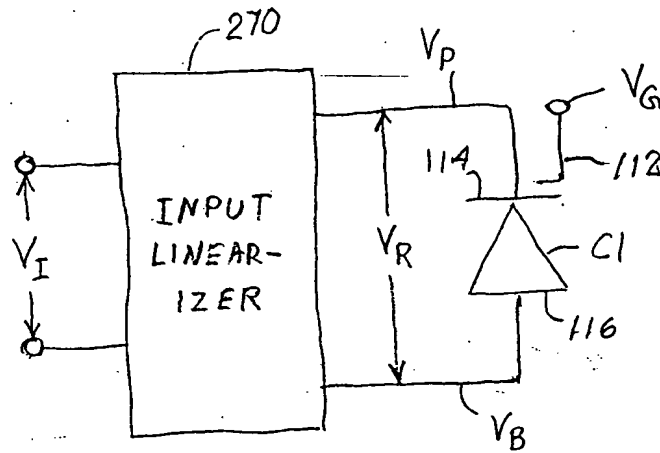


Fig. 35

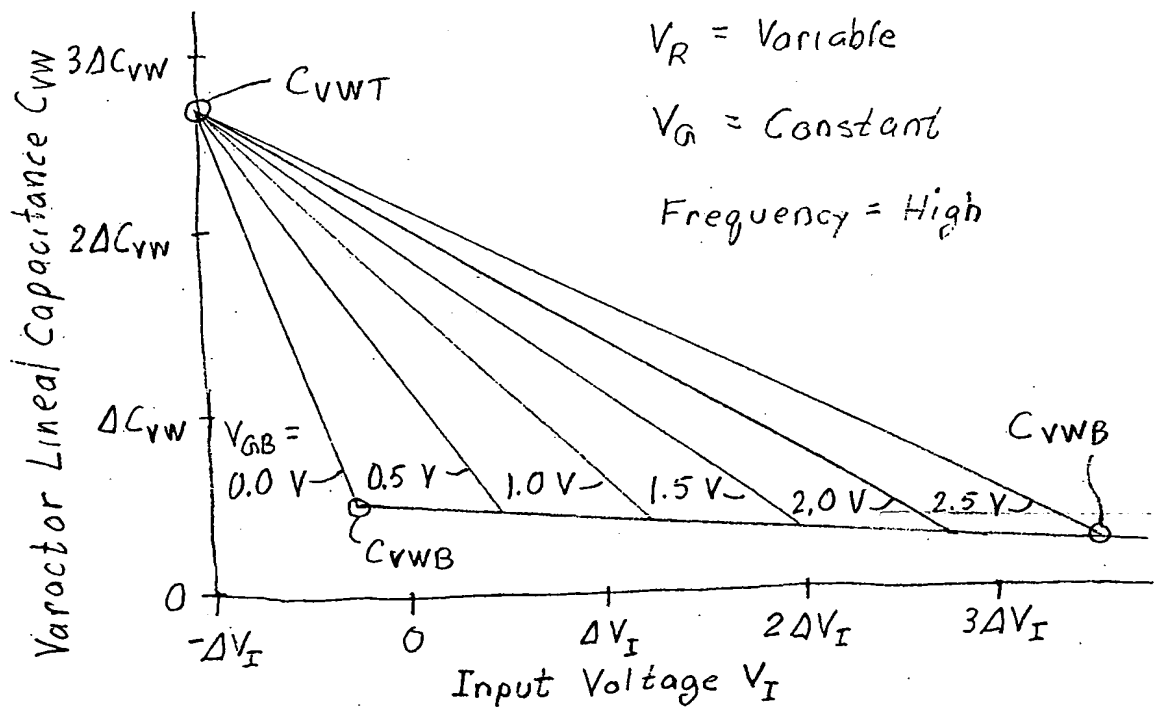


Fig. 36

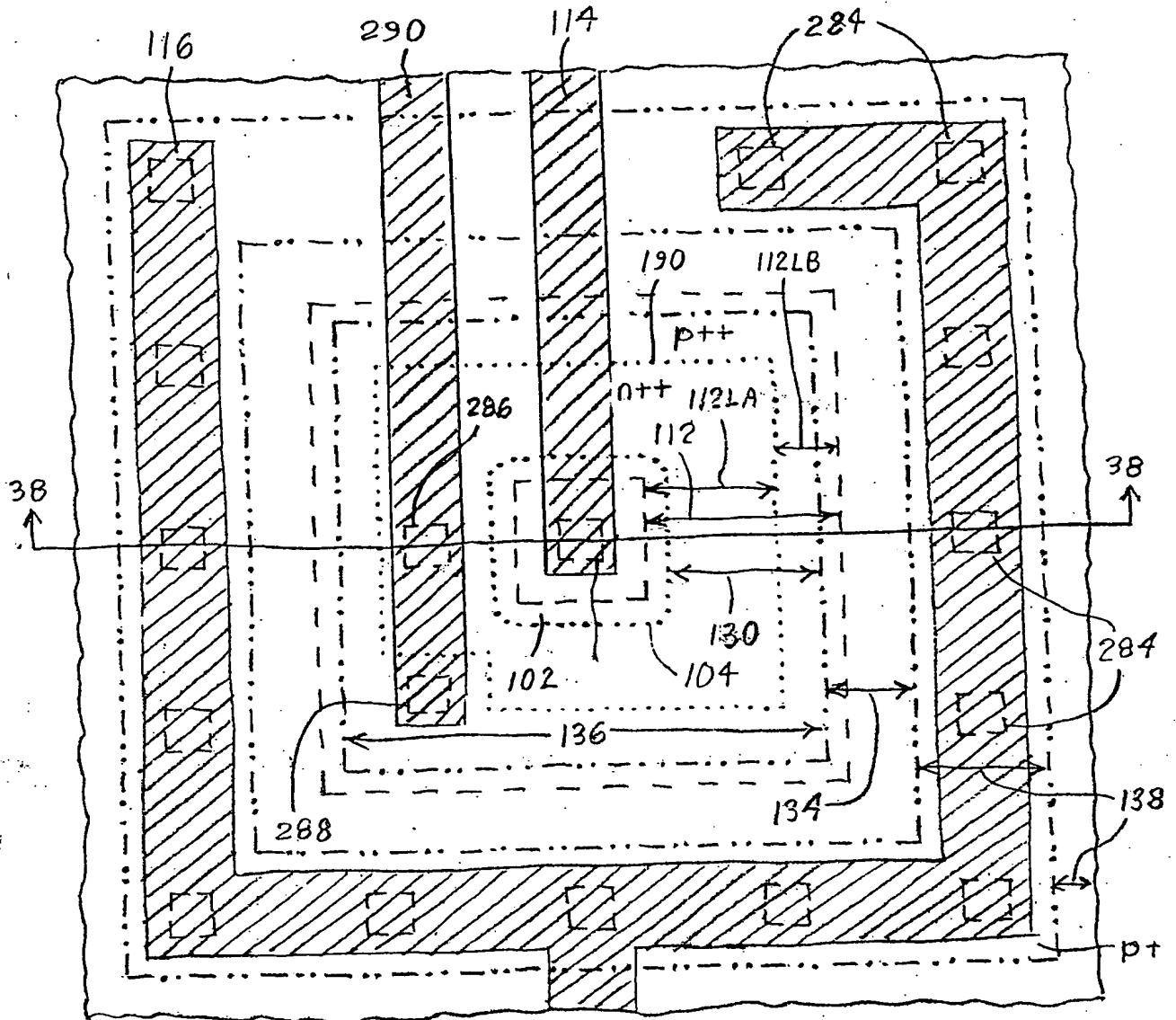


Fig. 37

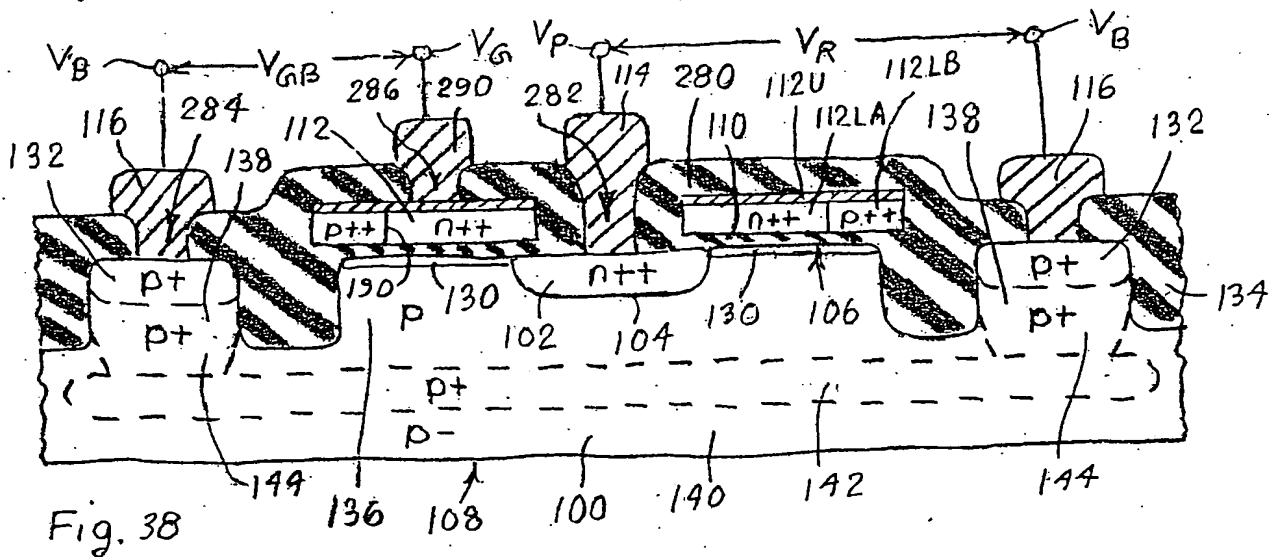


Fig. 38